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O MIR ON	er the Paperwork Reduction	n Act of 1995, no persons	s are required to re	Application Numb	Trademark Office: U.S. DEPARTMENT OF COMMERCE formation unless it displays a valid OMB control number. 109/652,550		
MATRORA	TRANSMITT			Filing Date	August 31, 2000		
	FORM			First Named Inven	ntor Keiji Jono		
	(to be used for all correspondence after			Group Art Unit	2811		
				Examiner Name	T. Tran		
	Total Number of Pages in This Submis			Attorney Docket Nu	umber KM1-001		
	ENCLOSURES (check all that apply)						
	Fee Transmittal Form Fee Attached X Amendment / Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53		Assignment Papers (for an Application) Drawing(s) Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Request for Refund CD, Number of CD(s) Remarks NO FEES REQUIF The Commissioner is hereicany additional fees requirations 1.16 and 1.17 and credit 23-0925		Other Enclosure(s) (please identify below): Postcard QUIRED. ereby authorized to charge equired under 37 CFR Sections		
	SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT						
Firm or Indiv	n vidual name	Frederick M. Fliegel, Ph.D Reg. No. 36,138 Wells, St. John, Roberts, Gregory & Matkin P.S. Customer No. 021567					
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Date	e	(October 26,2001					
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l her mail	I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:						
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#94 (NE) Amdt Spaus 7/22/oz

Application Serial No	August 31, 2000
AssigneeMicron Technology, Inc	
Group Art Unit	2811
Examiner	
Attorney's Docket No	KM1-001
Title: Methods of Forming an Isolation Tren of Forming an Isolation Trench in a Su of Forming an Isolation Trench-Isola Transistor, Trench Isolation Structure Memory Cells and DRAMS	ch in a Semiconductor, Methods rface of a Silicon Wafer, Methods ated Transistor, Trench-Isolated

RESPONSE TO OCTOBER 12, 2001 OFFICE ACTION

To:

Box Non-Fee Amendment

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

Frederick M. Fliegel, Ph.D.

(Tel. 509-624-4276; Fax 509-838-3424)

Wells, St. John, Roberts, Gregory & Matkin P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Sir:

Responsive to the Office Action dated October 12, 2001, Applicant amends and remarks as follows:

AMENDMENTS